

phyCORE®-STM32MP15x Hardware Changes Description PL1534.0 (alpha) vs. PL1534.1 (series)

The information in this document is relevant to customers that bought the phyCORE-STM32MP15x Alpha Kit containing the module phyCORE-STM32MP15x (PCM-068.A0) PCB revision 1534.0

Description:

This document describes the HW-changes of the phyCORE®-STM32MP15x from PCB revision PL1534.0 (alpha) to revision PL1534.1

Changes:

The following STM32MP1 Port connection to the phyCORE Connector X1 changed as shown below:

phyCON X1	PL1534.0 (alpha)	PL1534.1	Note
	X_I2C2_SDA/PH5		new Port
X1.D22	X_I2C4_SDA/PZ5*	X_I2C4_SDA/PZ5	connection on X1
			new Port
X1.D25	X_I2C4_SMBALERT/PZ2	Х_РН5	connection on X1
	X_USART1_RTS		new Port
X1.A56	X_I2C4_SDA /PZ5*	X_SPI16_MOSI/PZ2	connection on X1

Changes on PL1534.0

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^{*}X1.D22 Port PH5 to PZ5 -> Port change with wire-wrap

^{*}X1.A56 PZ5 function is X_I2C4_SDA in BSP (don't use on baseboard)