PHYTEC

Errata for

the phyCORE-OMAP44xx

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phyCORE-OMAP44xx Errata

This document details all known errata for the phyCORE-OMAP44xx. Table 1 provides a revision history for this document.

Tabelle 1: Document Revision History

Rev. Number	Date	Substantive Change(s)
1	20/11/2012	• Add a new SDMMC erratum, PCM049err1
0	20/11/2012	Initial Release

Table 2 summarizes all known errata on the phyCORE-OMAP44xx device, PL1347.1 and PL1347.2

Tabelle 2: Summary of Errata

Errata	Name	Solution	Page
SDMMC			
PCM049err1	Warm Reset Error	Software fix	

1. Erratas

1.1 SDMMC

PCM049err1 Warm Reset Error

Projected Impact: Detection of or booting from 3.3 V SD-cards may sporadically fail after a warm reset.

Description: - During a warm reset the voltage VCC_MMC1 (generated by the PMIC) does not reach 0 Volt and droppes only to 1.8 V.
- The default pulse width of the warm reset which is generated by the controller is 187.5 us. The SD-card specification requires a pulse of 1 ms under 0.5 V.
Due to these two causes the SD Card might reach an undefined reset stage which can result in lockup of the card.

Error Type: general at all Versions of phyCORE-OMAP44xx

Schematic: up PL1347.0

Workaround: Software workaround starting with PD12.2.0 (Barebox + Kernel).
After the first successful cold boot the PMIC is reconfigured, to prevent a general SD CARD reset during warm reset.
BSP download:
Patch download: