

PhyCORE-AM335x PD15.1.0 Release Notes

Operating System	Linux
BSP Release Status	RELEASED
Release Date	08 Jun 2015
Repository	
Binaries	ftp://ftp.phytec.de/pub/Software/Linux/BSP-Yocto-AM335x/BSP-Yocto-AM335x-PD15.1.0/images/
Source Archive	
Release Notes	Click Here



This software release is maintained by www.phytec.de.

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Introduction

This BSP provides a basis for development, deployment and execution of Linux based applications on the phyCORE-AM335X System on Module (SOM). For detailed information on the various software components included in the release and how to use them, please refer to the [Quickstart](#).

Versioning

Software

Component	Version
Linux Kernel	3.12.30
Yocto	1.7 (Dizzy)
OpenGL	OpenGL ES 2.0 (PowerVR SGX chipset driver 5.01.01.02)
Qt	5.3.2
barebox	2015.02.0

Compatible Hardware

BSP Release Version	BSP Release Date	SOM PCB Version	CB PCB Version
phyCORE-AM335x-PD15.1.0	08 Jun 2015	1358.3	1359.3

BSP Download

Prebuilt images of PD15.1.0 can be downloaded from the link below:

<ftp://ftp.phytec.de/pub/Software/Linux/BSP-Yocto-AM335x/BSP-Yocto-AM335x-PD15.1.0/images/>

Quickstart

Quickstarts for phyCORE-AM335x-PD15.1.0:

[PhyCORE-AM335x RDK Linux Quickstart PD15.1.x](#)

BSP Features

Interface	Detail	Implemented	Tested	Notes
UART	uart0	Yes	Yes	enabled - (RS-232) on Connector X18
	uart1	Yes	Yes	disabled - Signals conflicts with can1
	uart2	Yes	Yes	enabled - Expansion Connector X5
	uart3	Yes	Yes	enabled - Expansion Connector X5
	uart4	Yes	No	[click for info]
	uart5	Yes	No	[click for info]
I2C	i2c0	Yes	Yes	enabled - Expansion Connector X5
	i2c1	Yes	No	[click for info]
	i2c2	Yes	No	[click for info]
Ethernet	10/100 on SoM	Yes	Yes	enabled - RMII Mode
	10/100/1000 on CarrierBoard	Yes	Yes	enabled - RGMII Mode
MCASP	mcasp0	Yes	Yes	disabled - No Supported Audio Codec
	mcasp1	Yes	No	[click for info]
MMC/SDIO	mmc0	Yes	Yes	enabled - SD/MMC Card on Connector X20
	mmc1	Yes	No	[click for info]
	mmc2	Yes	No	[click for info]
Communication	TiWi-BLE Bluetooth	No	No	Planned for Future
	TiWi-BLE WiFi	No	No	Planned for Future
USB	usb0	Yes	Yes	enabled - USB 2.0 Mini-AB OTG Connector X7
	usb1	Yes	Yes	enabled - USB 2.0 Standard-A Connector X8 (Host)
CAN	can0	Yes	No	[click for info]
	can1	Yes	Yes	enabled - DB9 Male Connector X13
SPI	spi0	Yes	Yes	enabled - Expansion Connector X5
	spi1	Yes	No	[click for info]
Display and Touch	LCD (24-bit Interface)	Yes	Yes	enabled - Requires LCD-018 option Connectors X4 and X31
	Analog LCD Touch	Yes	Yes	enabled - Capacitive: ETM-FT5x06 (on LCD-018) Touch Controller - i2c_ts (i2c0)
	HDMI	Yes	Yes	disabled - Display pins configured for LCD
	Backlight	Yes	Yes	enabled - Requires LCD-018 option

GPIO	User Buttons and LEDs	Yes	Yes	User buttons: btn1, btn2 User leds: led1, led2 Bus enable GPIOs: gpio1_8 and gpio1_9 pulled LOW
Memory	8/16-bit NAND Flash (GPMC)	Yes	Yes	enabled
	SPI NOR Flash	Yes	Yes	enabled - serial_flash on spi0
	EEPROM	Yes	Yes	enabled - at24c32 on i2c0
RTC	External RTC	Yes	Yes	enabled - RV-4162-C7 on i2c0
	PMIC RTC	Yes	Yes	enabled - TPS65910A3 on i2c0
	AM335x RTC	No	No	Planned for Future
Power Management	PMIC	Yes	Yes	enabled - TPS65910A3 on i2c0
Audio	WM8974 on CarrierBoard	N/A	N/A	Not Implemented
JTAG	JTAG	N/A	N/A	Connector X21

[1] It may be possible to change the software configuration to utilize this interface even if it is not being set in the board's default configuration. Please see the Pin Attributes listed in [TI's AM335x datasheet](#) for more information on the various modes each pin can be muxed to.

Fixed In This Release

N/A

New In This Release

N/A

Known Issues

PHYTEC Known Issues

- No TiWi-Wifi support
- No TiWi-BLE support
- No Audio Support

Texas Instruments Known Issues

- See [here](#) for a list of TI's known issues for their SDK 7.0 kernel 3.12 release. PD15.1.0 is based on kernel 3.12.30 from TI.

Technical Support

For further support please visit [PHYTEC's Support Portal](#)