

phyCORE-i.MX6 HDMI+LVDS/ HDMI+LVDS Dual Display (same content)



PD21.1.1

imx6qdl-phytec-mira.dtsi "ddc-i2c-bus = <&i2c2>;"

BSP

```
~/BSP-Yocto-Ampliphy-i.MX6-PD21.1.1$ source sources/poky/oe-init-build-env
~/BSP-Yocto-Ampliphy-i.MX6-PD21.1.1/build$ bitbake linux-mainline -c devshell      ##terminalterminal
~/BSP-Yocto-Ampliphy-i.MX6-PD21.1.1/build/tmp/work-shared/phyboard-mira-imx6-3/kernel-source# cd arch/arm/boot
/dts/
~/BSP-Yocto-Ampliphy-i.MX6-PD21.1.1/build/tmp/work-shared/phyboard-mira-imx6-3/kernel-source/arch/arm/boot/dts#
vim imx6qdl-phytec-mira.dtsi      ##ddc-i2c-bus = <&i2c2>;
~/BSP-Yocto-Ampliphy-i.MX6-PD21.1.1/build/tmp/work-shared/phyboard-mira-imx6-3/kernel-source/arch/arm/boot/dts#
exit      ##terminal
~/BSP-Yocto-Ampliphy-i.MX6-PD21.1.1/build$ bitbake linux-mainline -c compile -f && bitbake linux-mainline -c
deploy      ##kernel
```

zImageoftree

HDMIbareboxLVDS

```
nv linux.bootargs.video="video=HDMI-A-0:1024x768"
saveenv
```

HDMI1024x768hdmi

```
nv linux.bootargs.video="video=HDMI-A-1:1024x768"
saveenv
```

HDMILVDS