

phyCORE-i.MX8M Plus / phyCORE-i.MX8M Plus hardware design info

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- RESET
- IO
-
-
- ALPHA

<https://www.phytec.de/produkte/system-on-modules/phycore-imx-8m-plus/#downloads/>

Hardware Manualimx8mp(i.MX 8M Plus Hardware Developer's Guide Datasheet)

[i.MX8M Plus Document on nxp.com](#)

- - PCM-070 PCM-070 phyCORE-i.MX8M Plus
 - PCM-070 PCB1549
 - PolluxPBA-C-17 phyBOARD Pollux
 - PolluxPCB1552
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- - PCM-070 Hardware Manual Technical Specifications
- - 120pin [REF-177862-03](#) (VM240)
 - 60pin BTH-030-01-L-D-A-K-TR (VM368)
 - [/ SOM connector information](#)
-
- /PCM-070 Hardware Manual Technical Specifications
- Altium Designer
-
- dxf/step 3D

imx8mp

IO bankIO

imx8mp datasheet 3.2.1 Power-Up Sequence

[External Logic IO Supply Voltage](#)

X_nPWR_READY	C14

12V

RESET

X_PMIC_RST_B	C11

reset

reset

50ms

IO

IO

X_VIO_Ctrl	D8

X_VIO_Ctrl	IO
3.3V	3.3V
GND	1.8V

IOX_VIO_Ctrl

X_SAI1_XXX	A7 ~ A14	1.8V
X_SAI5_XXX	E18 ~ E24	1.8V
X_ENET_XXX	A15 ~ A32	1.8V

X_BOOT_MODE[3:0]	D25 D24 D23 D22

Boot Mode Selection

Boot Mode	X_BOOT_MODE3	X_BOOT_MODE2	X_BOOT_MODE1	X_BOOT_MODE0	Boot Source
0	0	0	0	0	Boot from internal fuses
1	0	0	0	1	USB Serial Downloader
2	0	0	1	0	Boot from onboard eMMC U4
3	0	0	1	1	Boot from ext. SD-Card on SD2
4	0	1	0	0	Boot from onboard QSPI Flash U5
15	1	1	1	1	JTAG mode

100k0 internal fuses4.7K

ioVDD_IO

X_POR_B	C13

gpio

uart1bootloader Linux kernel

X_UART1_RXD	A2
X_UART1_TXD	A3

M7

X_UART4_RXD	B4
X_UART4_TXD	B5

ALPHA

1. X_SAI1_XXXX_SAI5_XXXX_ENET_XXXX_VIO_Ctrl
2. UART2
3. X_POR_B 1.8VX_VIO_Ctrl