

phyCORE-AM57xx-PD15.1-rc[1|2|3|4] BSP Reference Guide

- [Boot Options](#)

Boot Options

The phyCORE-AM57xx SOM is configured to boot from SD then eMMC by default. This can be overwritten by setting DIP switch S5 on the PCM-948 carrier board as shown in the phyCORE-AM57xx RDK Boot Modes section of the table below.

Interface	Detail	Implemented in U-Boot	Implemented in Kernel /RFS	phyCORE-AM57xx RDK Boot Modes (DIP Switch S5)
eMMC	Boot Order: eMMC, USB	Yes	Yes	1 2 3 4 5 6 7 8 ----- off on off on off on off on
NAND	Boot order: NAND, USB	Yes	No	1 2 3 4 5 6 7 8 ----- on off off on off on off on
SD	Boot order: SD, eMMC, USB	Yes	Yes	1 2 3 4 5 6 7 8 ----- off on on off off on off on
SPI Flash	Boot order: QSPI_1, SD, USB	Yes	No	1 2 3 4 5 6 7 8 ----- off on on off on off off on
Network	N/A	No		N/A

[1] Only a subset of possible boot configurations are listed. For a complete list of the AM57xx boot modes please refer to the Texas Instruments AM57xx Technical Reference Manual.