

phyCORE-i.MX6 Q&A

- QSD
- Qcpu
- Qfb0
- QHDMI+LVDS
- Q
- QMAC
- Q: openc1?

QSD

A SD3.0SDXCUHS-lbspexfatwindows32gbfat32linuxext4fat32windows

Qcpu

A /sys/class/thermal/thermal_zone0/temp

```
echo $((`cat /sys/class/thermal/thermal_zone0/temp|cut -c1-2`)).$((`cat /sys/class/thermal/thermal_zone0/temp|cut -c3-5`))
```

<http://kernelreloaded.com/formatting-output-from-raspberry-pi-temperature-sensors/>

<https://cache.nxp.com/docs/en/application-note/AN4579.pdf>

Qfb0

Abootargs imxdrm.legacyfb_depth=32

bootargs nv linux.bootargs.video=video=LVDS-1:1280x800-32

QHDMI+LVDS

APD16.1.1

Q

APD16.1.0hdmi+lvds0001-dts-imx6qdl-phytec-mira-Fix-typo-in-HDMI-node.patch

QMAC

https://www.phytec.de/documents/l-814e-3-imx6-bsp-manual/#On-Chip_OTP_Controller_28OCOTP_CTRL29_-_eFuses

```
root@phyboard-mira-imx6-3:~# devmem2 0x21bc620
/dev/mem opened.
Memory mapped at address 0xb6f16000.
Read at address 0x021BC620 (0xb6f16620): 0xF40A378C
root@phyboard-mira-imx6-3:~# devmem2 0x21bc630
/dev/mem opened.
Memory mapped at address 0xb6fbe000.
Read at address 0x021BC630 (0xb6fbe630): 0x0000502D
```

MAC:
50:2D:F4:0A:37:8C

Q: openc1?

A: phytec-qt5demo-image.bb

```
imx-gpu-viv-demos \
libopenc1-mx6 \
```