BSP Yocto Yogurt i.MX6 PD16.1.1 Release Notes

Operating System	Linux
BSP Release Status	RELEASED
Release Date	07 Feb 2018
Repository	PHYTEC Germany Public Repos
Binaries	ftp://ftp.phytec.de/pub/Software/Linux/BSP-Yocto-i.MX6/BSP-Yocto-i.MX6-PD16.1.1/images/
Kernel Source	https://git.phytec.de/linux-mainline/tree/?h=v4.1.46-phy2
Quickstart Guide	BSP Yocto i.MX6 Manual PD16.1.x

⚠ This software release is maintained by www.phytec.de.

Introduction

This BSP provides a basis for development, deployment and execution of Linux based applications on the i.MX6 System on Module (SOM). For detailed information on the various software components included in the release and how to use them, please refer to the Quickstart.

Versioning

Software

Component	Version
Linux Kernel	v4.1.46
Yocto	2.1.3 (Krogoth)
Qt	5.6.3
Barebox	2016.11.0
Host OS	Tested on 64-bit Ubuntu 14.04 LTS
Tested Image	phytec-qt5demo-image

Supported Hardware

- phyFLEX-i.MX6 SOM and Kit (Quad, DualLite, and Solo)
- Single Board Computers:

    - phyBOARD-Mira (Quad, Quad Plus, DualLite, Solo)
    - phyBOARD-Subra

BSP Download

Prebuilt images of PD16.1.1 can be downloaded from the link below:

ftp://ftp.phytec.de/pub/Software/Linux/BSP-Yocto-i.MX6/BSP-Yocto-i.MX6-PD16.1.1/images/

Quickstart

Quickstarts for Yocto i.MX6: BSP Yocto i.MX6 Manual PD16.1.x

Fixed In This Release

- **Kernel**

    - CAN is now working with 10 kbaud
    - A bug was fixed where the LVDS display sometimes did not show anything after boot. This bug is described in erratum ERR009219
    - Fixed a typo in the HDMI devicetree node which prevented DDC to work correctly.
    - The npwrdn GPIO assignement on the phytec-mediabus was fixed from GPIO5_27 to GPIO3_9 on the phyBOARD-Mira.
    - Fixed a bug described by erratum ERR007117 which led to an infrequent detection failure of the NAND flash during boot.
    - Fixed a GPU stability issue where the GPU clocks run on clock rates which were to high by default.
    - Fixed a stability issue when switching the internal LDOs of the i.MX 6DualLight into bypass mode.
    - Introduced small performance improvements by disabling spinlock debug logging which is enabled by default.

- **Barebox**

    - Barebox devicetree now supports environment on SPI-NOR flash
    - Fixed the CS0_END RAM setting for the i.MX 6QuadPlus
    - Fixed a bug described by erratum ERR007117 which led to an infrequent detection failure of the NAND flash during boot.
    - Improved RAM timings for i.MX6 DualLite\Solo (400 MHz) on all phyFLEX machines.

New In This Release

- Added support for ISSI and Winbond NOR flash

    - Winbond W25Q128
    - ISSI IS25LP128
- Added support for Winbond NAND
- Linux kernel upgraded to v4.1.46 (from v4.1.36)

Not Supported

N/A

Known Issues

PHYTEC Known Issues

- **Kernel**
    - The LDB_DI0_SEL and LDB_DI1_SEL mux can only be set in the devicetree and cannot be changed on runtime
    - QuadPlus:

- Display and HDMI do not work

Technical Support

For further information or to report any problems, visit the PHYTEC Support Portal